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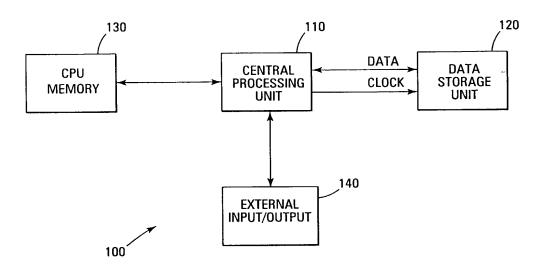


Fig. 1

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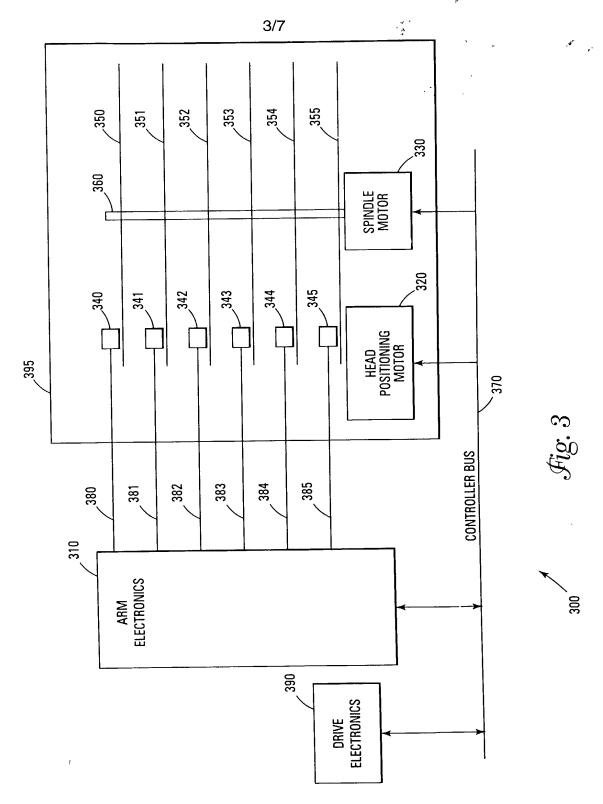
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DATA CLOCK CONTROLLER 280 290 210 250 240 260 230

Fig. 2

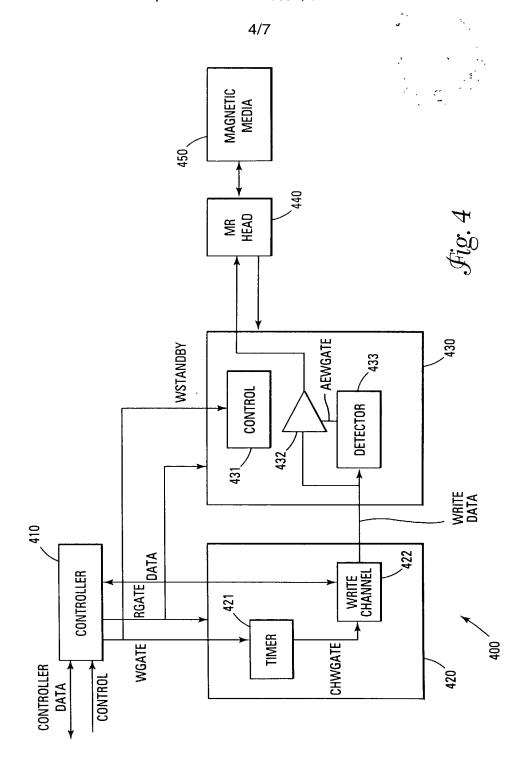
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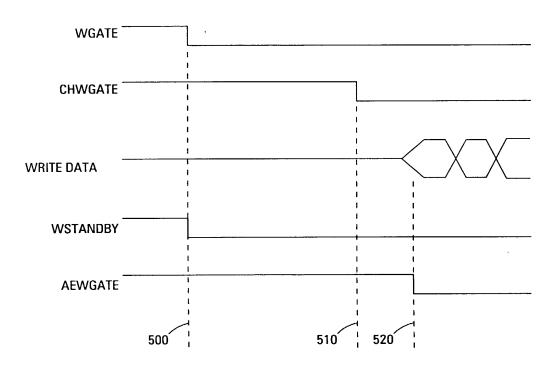
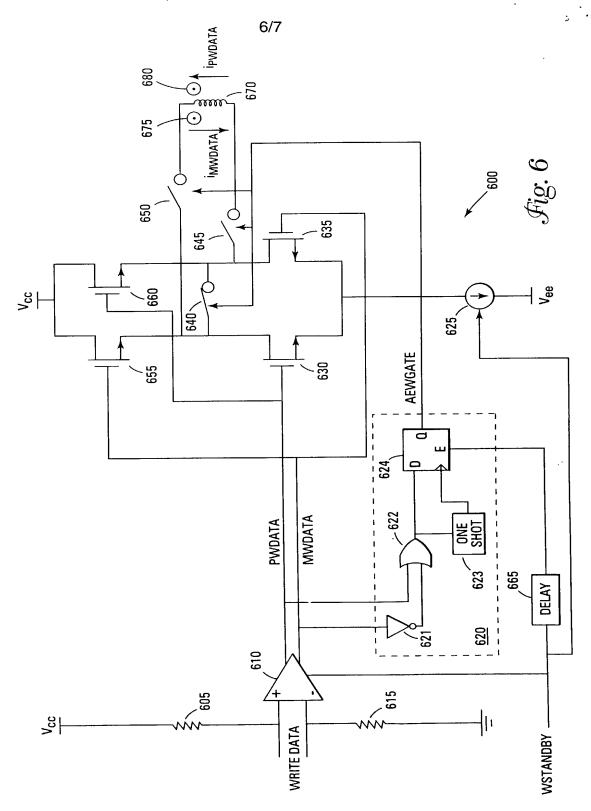


Fig. 5

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